## **2.5 Volt Reference**

The CS1009 is a precision trimmed 2.5 V  $\pm$ 5.0 mV shunt regulator diode. The low dynamic impedance and wide operating current range enhances its versatility. The tight reference tolerance is achieved by on–chip trimming which minimizes voltage tolerance and temperature drift.

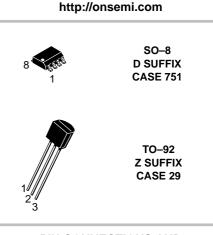
A third terminal allows the reference voltage to be adjusted  $\pm 5.0\%$  to calibrate out system errors. In many applications, the CS1009GZ can be used as a pin-to-pin replacement of the LT1009CZ and the LM136Z-2.5 with the external trim network eliminated.

### Features

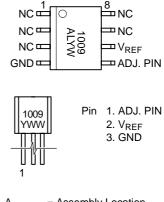
- 0.2% Initial Tolerance Max.
- Guaranteed Temperature Stability
- Maximum 0.6 Ω Dynamic Impedance
- Wide Operating Current Range
- Directly Interchangeable with LT1009 and LM136 for Improved Performance
- No Adjustments Needed for Minimum Temperature Coefficient
- Meets Mil Std 883C ESD Requirements

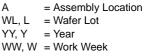


## ON Semiconductor<sup>™</sup>



### PIN CONNECTIONS AND MARKING DIAGRAM





### **ORDERING INFORMATION**

Device	Package	Shipping
CS1009GD8	SO-8	95 Units/Rail
CS1009GDR8	SO–8	2500 Tape & Reel
CS1009GZ3	TO-92	2000 Units
CS1009GZR3	TO-92	2000 Tape & Reel

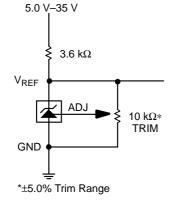
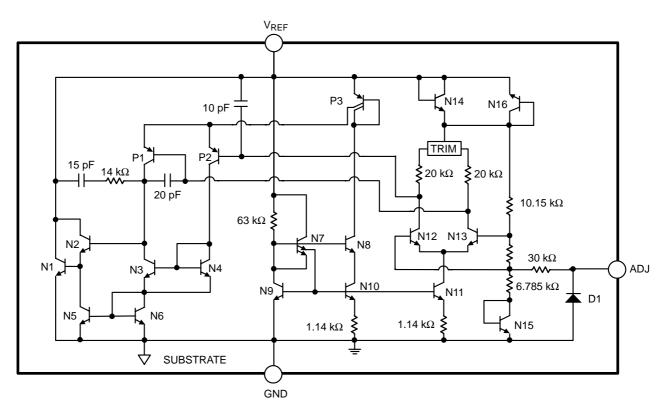


Figure 1. Application Diagram







### **MAXIMUM RATINGS\***

Rating	Value	Unit	
Reverse Current		20	mA
Forward		10	mA
Operating Temperature Range		-40 to 105	°C
Storage Temperature Range		-65 to +150	°C
Lead Temperature Soldering:	Wave Solder (through hole styles only) (Note 1) Reflow: (SMD styles only) (Note 2)	260 peak 230 peak	°C ℃

1. 10 second maximum

2. 60 second maximum above 183°C.

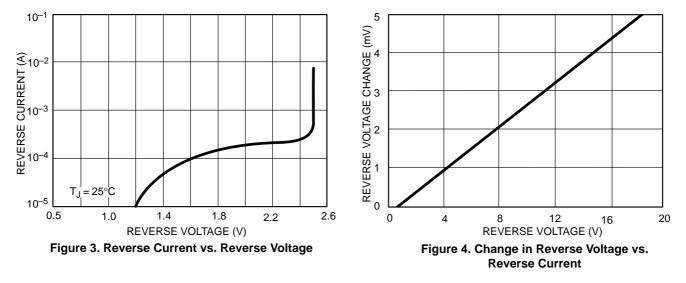
\*The maximum package power dissipation must be observed.

Characteristic	Test Conditions		Min	Тур	Max	Unit
Reverse Breakdown Voltage	I <sub>R</sub> = 1.0 mA		2.492	2.500	2.508	V
Reverse Breakdown Voltage	$0^\circ C \leq T_A \leq 105^\circ C$		2.492	2.500	2.508	V
Reverse Breakdown Voltage	$-40^\circ C \le T_A \le {}^\circ C$		2.480	2.500	2.508	V
Reverse Breakdown Voltage Change with Current	$400 \ \mu A \leq I_R \leq 10 \ mA$	†		2.6 3.0	10 12	mV mV
Reverse Dynamic Impedance	I <sub>R</sub> = 1.0 mA	†		0.2 0.4	1.0 1.4	Ω Ω
Temperature Stability Avgerage Temperature Coefficient	$\begin{array}{l} 0^{\circ}C \leq T_{A} \leq 70^{\circ}C, \text{ Note 3} \\ 0^{\circ}C \leq T_{A} \leq 70^{\circ}C, \text{ Note 3} \end{array}$			- -	- -	mV ppm/°C
Long Term Stabilty	$T_A = 25^{\circ}C \pm 0.1 \text{ C}, I_R = 1.0 \text{ mA}$		-	20	-	ppm/kHr

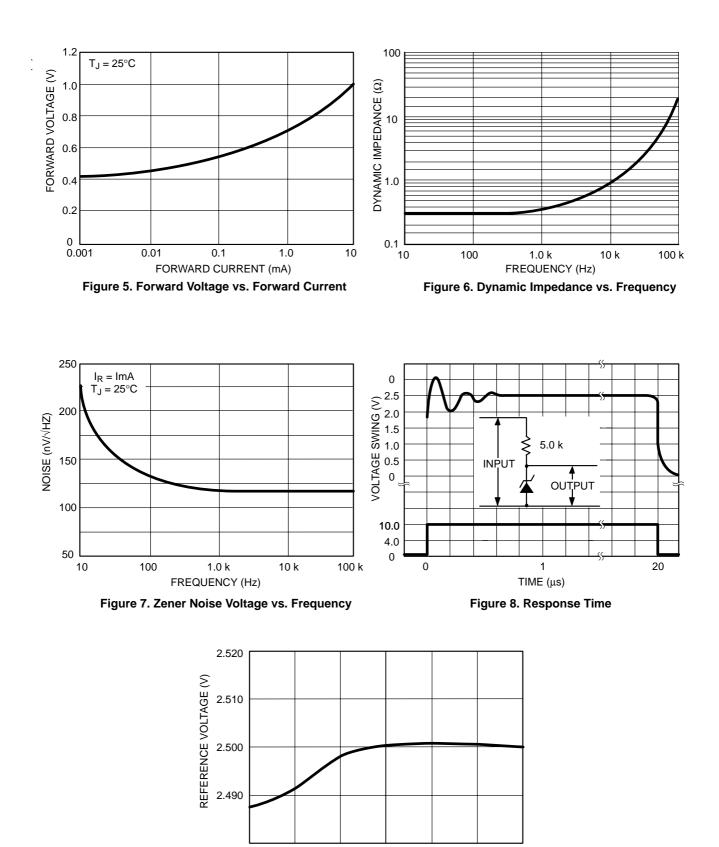
### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise specified.)

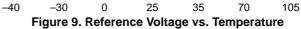
† Denotes the specifications which apply over full operating temperature range.

3. Average temperature coefficient is defined as the total voltage change divided by the specified temperature range.



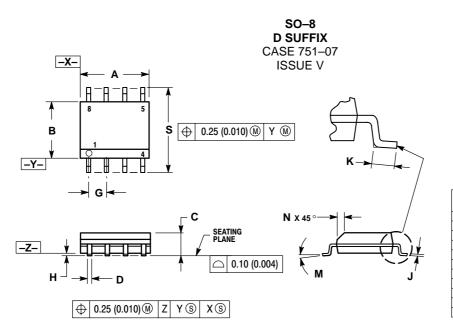
## **TYPICAL PERFORMANCE CHARACTERISTICS**







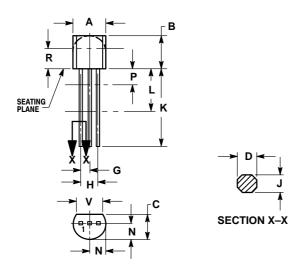
## PACKAGE DIMENSIONS



	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.80	5.00	0.189	0.197	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.053	0.069	
D	0.33	0.51	0.013	0.020	
G	1.27 BSC		0.050 BSC		
Н	0.10	0.25	0.004	0.010	
J	0.19	0.25	0.007	0.010	
Κ	0.40	1.27	0.016	0.050	
М	0 °	8 °	0 °	8 °	
Ν	0.25	0.50	0.010	0.020	
S	5 80	6 20	0 228	0 244	

TO-92 **Z SUFFIX** CASE 29-11 **ISSUE AL** 

D



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUL OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
Κ	0.500		12.70		
L	0.250		6.35		
Ν	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
V	0.135		3.43		

#### PACKAGE THERMAL DATA

Parameter		SO-8 TO-92		Unit	
$R_{\Theta JC}$	Typical	45	-	°C/W	
$R_{\Theta JA}$	Typical	165	170	°C/W	

# <u>Notes</u>

# <u>Notes</u>

**ON Semiconductor** and without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

#### PUBLICATION ORDERING INFORMATION

#### Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031 Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.